



Docket No.: 08211/0200374-US0/P05815
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Ha Chu Vu

Allowed: May 6, 2005

Application No.: 10/717,955

Confirmation No.: 9020

Filed: November 20, 2003

Art Unit: 2816

For: PRESCALING FOR DIVIDING FAST
PULSED SIGNAL

Examiner: H. L. Nguyen

COMMENTS ON STATEMENT OF REASONS FOR NOTICE OF ALLOWANCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice of Allowance dated May 5, 2005, Applicant respectfully submits the following comments on the statement of reasons for allowance.

The Action stated, "The prior art rejection to the claims made in the previous Office Action mailed on 12/01/2004 are now withdrawn in view of Applicant's amendments." As far as is known to Applicant, what is meant by this is that the prior art rejection was withdrawn in view of arguments made in the Remarks section of the Amendment filed on February 22, 2005, rather than the actual claim amendments that were made in the Amendment.

Applicant respectfully submits that the amendments made to claims 1, 9, and 10 in the Amendment filed on February 22, 2005 did not change the scope of the claims and were not made for purposes of patentability. Rather, the amendments to Claim 1, 9, and 10 were cosmetic amendments made to make the meaning of the claims clearer to the reader. More specifically, the phrase "the modulus control signal is ignored at least until the onset of the next input pulse signal is

received” was changed to “the modulus control signal is ignored at least until **after** the onset of the next input pulse signal is received” (emphasis added). Applicant respectfully submits that the addition of the word “after” does not change the meaning or scope of the claim, but makes the meaning of the claim clearer to the reader.

Further, the Action states, “Note that as shown in the present application (FIG. 9), a period of time, CPD9 962, occurs from the M^{th} pulse of signal clkin (faster) to the falling edge of modulus control signal DIV4, where $M=(P*N+S)$. As shown in the figure, time CPD9 962 is sufficiently large that the next pulse ($(M+1)^{\text{th}}$ pulse) of signal clkin (faster) occurs prior to the falling edge of modulus control signal DIV4. However, the prescaler ignores modulus control signal DIV4 at the onset of the $(M+1)^{\text{th}}$ pulse of signal clkin (faster) (see page 8, lines 27-30 of the specification). Accordingly, in this embodiment, the fact that time CPD9 962 has not elapsed by the onset of the $(M+1)^{\text{th}}$ pulse is irrelevant. Because the modulus control signal is ignored by the prescaler until after the onset of the next pulse after the M^{th} pulse (where the M^{th} pulse is the $(P*N+S)^{\text{th}}$ pulse), the period of input signal clkin may be less than period of time CPD9 962 (which is the period of time from the $(P*N+S)^{\text{th}}$ pulse of input signal clkin to the falling edge of modulus control signal DIV4).”

Applicant respectfully submits that the invention as claimed is not limited to the illustrative embodiment discussed above, and that other embodiments are within the scope and spirit of the invention as claimed.

Dated: July 28, 2005

Customer No.: 38845

Respectfully submitted,

By 

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Part B – Fee(s) Transmittal (1 page)
Comments on Statement of Reasons for Notice of Allowance (2 pages)
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